

# Shortest Path Feasibility Algorithms: An Experimental Evaluation

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## Abstract

This is an experimental study of algorithms for the shortest path feasibility problem: Given a directed weighted graph, find a negative cycle or present a short proof that none exists. We study previously known and new algorithms. Our testbed is more extensive than those previously used, including both static and incremental problems, as well as worst-case instances. We show that, while no single algorithm dominates, a small subset (including a new algorithm) has very robust performance in practice. Our work advances state of the art in the area.

## 1 Introduction

The *shortest path feasibility problem* (FP) is to find a negative-length cycle in a given directed, weighted graph, or to present a proof (a set of feasible potentials) that no such cycle exists. This is closely related to the problem of finding shortest path distances in a network. A solution to the shortest path problem is also a solution to FP. Furthermore, given a feasible set of potentials, one can solve the shortest path problem in almost linear time with Dijkstra’s algorithm [9]. These are classical network optimization problems with many applications, such as program verification [25] and real-time scheduling [5].

The classical Bellman–Ford–Moore (BFM) algorithm [1, 11, 18] achieves the best known strongly polynomial time bound for FP:  $O(nm)$  (where  $n$  and  $m$  denote the number of vertices and arcs in the graph, respectively). With the additional assumption that arc lengths are integers bounded below by  $-N \leq -2$ , the  $O(\sqrt{nm} \log N)$  bound of Goldberg [14] is an improve-

ment, unless  $N$  is very large. The BFM bound can also be improved in the expected sense for many input distributions; see *e.g.* [17].

Previous studies of shortest path algorithms include [2, 3, 8, 13, 19, 20, 21, 26]. The earliest experiments concluded that BFM performs poorly in practice, and favored alternatives such as incremental graph algorithms [20, 21] and the threshold algorithm [8]. These methods have relatively bad worst-case bounds, however, and are not robust in practice. More recently, Cherkassky and Goldberg [2] showed that two  $O(nm)$  algorithms, BFCT [23] and GOR [15], are much more robust. These methods, which have been studied further in [19, 26], serve as the basis of our experimental comparison.

Our first contribution is an algorithm design framework that generalizes BFM and is a natural basis for new algorithms.

As our second contribution, we implement several new algorithms and reimplement existing ones, in one case correcting a heuristic used in previous implementations. Our experiments show that a new algorithm, RD (Robust Dijkstra), is not only very robust, but also significantly outperforms previous ones on some problem classes.

Finally, we propose a new set of benchmark instances that is more extensive than existing ones. It includes natural problems, bad-case instances for all algorithms, and experiments modeling incremental scenarios where one solves a sequence of feasibility problems, each a perturbation of the previous one.

This paper is organized as follows. Section 2 presents some definitions and notation. Section 3 reviews the scanning method and gives a general framework for designing  $O(n)$ -pass algorithms. The negative cycle detection techniques used by the algorithms we implemented are discussed in Section 4. Section 5 reviews existing algorithms used in our study, including their improved implementations, and introduces some new algorithms. Section 6 describes families of instances that elicit the worst-case behavior of these methods. Section 7 describes our experimental setup and presents an empirical comparison of the various algorithms studied.

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Finally, Section 8 presents concluding remarks.

## 2 Definitions and Notation

The inputs to FP are a directed graph  $G = (V, E)$  and a length function  $\ell : E \rightarrow \mathbf{R}$ . A *potential function* maps vertices to values in  $\mathbf{R} \cup \{\infty\}$ . We refer to these values as *potentials*. Given a potential function  $d$ , we define the *reduced cost function*  $\ell_d : E \rightarrow \mathbf{R} \cup \{\infty\}$  by  $\ell_d(v, w) = \ell(v, w) + d(v) - d(w)$ . (If  $d(v) = d(w) = \infty$ , we define  $\ell_d(v, w) = \ell(v, w)$ .) We refer to arcs with negative reduced cost as *negative arcs*. The potential function is *feasible* if there are no negative arcs.

Note that replacing  $\ell$  with  $\ell_d$  may change the length of the shortest path between two distinct vertices, but the path itself (the sequence of arcs) will remain the same. It is also easy to see that this change (a *potential transformation*) preserves the length of every cycle.

The goal of FP is to find a feasible set of potentials or a negative-length cycle (or simply *negative cycle*) in  $G$ . A feasible potential function exists if and only if there is no negative cycle.

We say that an arc  $a$  is *admissible* if  $\ell_d(a) \leq 0$ , and denote the set of admissible arcs by  $E_d$ . The *admissible graph* is defined by  $G_d = (V, E_d)$ .

To deal with the fact that the input graph may not be strongly connected and that some algorithms for FP maintain a tentative shortest path tree, we add a *root vertex*  $s$  to  $V$  and connect  $s$  to all other vertices with zero-length arcs. Further references to  $G$  will thus be to this transformed graph. A *shortest path tree* of  $G$  is a spanning tree rooted at  $s$  such that for any  $v \in V$ , the  $s$ -to- $v$  path in the tree is a shortest path from  $s$  to  $v$  in  $G$ .

We say that  $d(v)$  is *exact* if the distance from  $s$  to  $v$  in  $G$  is equal to  $d(v)$ , and *inexact* otherwise.

## 3 Scanning Algorithms

This section briefly outlines the general *labeling method* [10] for solving shortest path and feasibility problems, on which all algorithms studied in this paper are based. (See *e.g.* [24] for more details.) We maintain for every vertex  $v$  its potential  $d(v)$  and parent  $p(v)$  (possibly *null*). Typically, all potentials are initially zero,  $p(s)$  is set to *null* and the remaining vertices  $v$  have  $p(v)$  set to  $s$ . At every step, we perform a *labeling operation*: pick an arc  $(u, v)$  such that  $d(u) < \infty$  and  $\ell_d(u, v) < 0$  and set  $d(v) = d(u) + \ell(u, v)$  and  $p(v) = u$ . If  $G$  has no negative cycle, eventually there will be no negative arcs, at which point  $d$  will be feasible. As Section 4 will show, a simple modification ensures that this method also terminates in the presence of a negative cycle.

The *scanning method* is a variant of the labeling method based on the SCAN operation. The

method maintains for each vertex  $v$  a *status*  $S(v) \in \{\text{unreached, labeled, scanned}\}$ . Given a labeled vertex  $v$ ,  $\text{SCAN}(v)$  examines all arcs  $(v, w)$ , and if  $d(v) + \ell(v, w) < d(w)$  then  $d(w)$  is set to  $d(v) + \ell(v, w)$ ,  $p(w)$  to  $v$ , and  $S(w)$  to labeled. A labeled vertex becomes scanned when a SCAN operation is applied to it, and a vertex of any status becomes labeled whenever its potential decreases. Scanning algorithms for FP typically start with all potentials set to zero, which requires the initial set of labeled vertices to contain all vertices with outgoing negative arcs.

**3.1  $O(n)$ -pass algorithms.** As described, the scanning algorithm does not necessarily run in polynomial time. We can, however, define a general class of scanning methods that perform  $O(nm)$  labeling operations. This class generalizes BFM and GOR, among other algorithms.

We partition the sequence of vertex scans into *passes* (subsequences of scans) with the following properties: (a) each vertex is scanned at most once during a pass; and (b) each vertex that is already labeled when the pass begins must be scanned during the pass. Note that a vertex that becomes labeled during the pass can also be scanned, but it does not need to be.

**LEMMA 3.1.** *If there is a shortest path from  $s$  to  $v$  containing  $k$  arcs, then after at most  $k$  passes  $d(v)$  is exact. Thus in the absence of negative cycles, the algorithm terminates after at most  $n - 1$  passes.*

The proof is by induction on the number of passes: one shows that after pass  $i$ , every vertex whose shortest path from  $s$  has  $i$  arcs will have exact potential.

BFM can be seen as an implementation of this general algorithm. It maintains labeled vertices in a queue: the vertex to be scanned next is removed from the front, and newly labeled vertices are inserted into the back. Note that every vertex that is currently labeled will be scanned before any vertex that is not, as required by a pass.

## 4 Negative Cycle Detection

To ensure that the labeling method terminates in the presence of negative cycles, we must use a *cycle detection strategy*. This section discusses some of them.

Let the *parent graph*  $G_p$  be the subgraph of  $G$  induced by the arcs  $(p(v), v)$ , for all  $v$  such that  $p(v) \neq \text{null}$ . This graph has several useful properties (see *e.g.* [24]). In particular, the arcs in  $G_p$  have nonpositive reduced costs and (if  $G_p$  is acyclic) form a tree rooted at  $s$ . If  $G_p$  does have a cycle, it will correspond to a negative cycle in the original graph. Conversely, if  $G$

contains a negative cycle, then  $G_p$  will provably contain a cycle after a finite number of labeling operations.

Although a cycle can appear in  $G_p$  after one labeling operation and disappear after the next, it can be detected as soon as it appears. Suppose a labeling operation is applied to an arc  $(u, v)$  and  $G_p$  is acyclic: a cycle will appear in  $G_p$  if and only if  $u$  is a descendant of  $v$  in the current tree. This could be tested by following parent pointers from  $u$ , but this takes  $\Theta(n)$  worst-case time and does not work well in practice [2].

A more efficient solution is *subtree disassembly*, proposed by Tarjan [23]. When the labeling operation is applied to  $(u, v)$ , one traverses the entire subtree rooted at  $v$ . If  $u$  is found, the algorithm terminates with a negative cycle. Otherwise, all vertices visited (except  $v$  itself) are removed from the current tree and marked as unreached. They will only be scanned after becoming labeled again. Disassembling a subtree takes linear worst-case time if we maintain a doubly-linked list to represent a preorder traversal of the current tree [16]. Fortunately, this cost can be charged to the work of building the subtree, which makes the amortized cost of each labeling operation constant. In particular, applying subtree disassembly to an  $O(n)$ -pass algorithm does not change its worst-case complexity. Marking a labeled vertex as unreached may delay its next scan, but this is arguably positive because its potential is known to be inexact. The net effect is usually a decrease in the total number of scans.

A slight variant of this method is *subtree disassembly with updates* [2]: if the potential of  $v$  decreases by  $\delta$ , potentials of all proper descendants  $w$  of  $v$  can be updated to  $d(w) - \delta$ . Because the new potentials may be exact, additional bookkeeping is required to make sure these vertices are scanned at least once more.

Here we propose a simpler alternative for the case when arc lengths are integral. Instead of decreasing the potentials by  $\delta$ , we decrease them by  $\delta - 1$ , to  $d'(w) = d(w) - \delta + 1$ . Because the new potentials are not exact, the updated vertices are guaranteed to be scanned again. Moreover, the optimization is still effective: after an update, only paths to  $w$  with length equal to  $d(w) - \delta$  (or shorter) will cause  $w$  to become labeled.

An alternative to methods based on subtree traversal is *admissible graph search* [14], which uses the fact that if  $p(w) = v$ , then  $(v, w)$  is in  $G_d$ . Therefore, if  $G_p$  contains a cycle, the admissible graph  $G_d$  contains a negative cycle. Since the arcs in  $G_d$  have nonpositive reduced cost, a negative cycle can be found in  $O(n + m)$  time using an augmented depth-first search. As we will see, admissible graph search is a natural strategy for GOR: since it already performs a depth-first search of

$G_d$  at each iteration, cycle detection has very little overhead.

## 5 Algorithms

This section describes the algorithms we tested, including both existing and new ones.

**5.1 BFCT.** This is the BFM algorithm with subtree disassembly, including updates. Initially, all vertices are labeled and have zero potential. Labeled vertices are maintained in a FIFO queue. A vertex to be scanned is removed from the front of the queue, and newly labeled vertices are inserted at the back. When an arc  $(u, v)$  is scanned and the potential of  $v$  is reduced, we disassemble and update the subtree rooted at  $v$ . Any labeled vertices found in the subtree are deleted from the queue, since they are no longer labeled. BFCT runs in  $O(nm)$  worst-case time.<sup>1</sup>

**5.2 GOR.** Proposed by Goldberg and Radzik [15], GOR is an  $O(n)$ -pass algorithm that works as follows. Define a *source* as a vertex with outgoing negative arcs. Suppose the admissible graph  $G_d$  is acyclic. Intuitively, improvements in potential values will propagate along the arcs of this graph. In each pass, GOR sorts in topological order all sources and the vertices reachable from them in  $G_d$ , then scans them in this order.

Because  $G_d$  is not necessarily acyclic, we compute its strongly connected components (SCCs). If there is an SCC containing a negative arc, it also contains a negative cycle, and GOR terminates. Otherwise, there is a zero-reduced-cost path between any two vertices within each SCC. Therefore, we can make  $G_d$  acyclic by contracting each SCC into a single vertex (until the end of the computation). Because of its performance overhead and implementation complexity, however, graph contraction should be avoided in practice.

The implementation of [2] avoids both the contraction and the SCC computation by running a simple depth-first search (DFS). When a back arc is discovered, it checks if the corresponding cycle is negative. If it is, the algorithm terminates; otherwise, it ignores the back arc and resumes the DFS. If no negative cycle is found, the DFS produces a topological ordering of the graph obtained from  $G_d$  when the ignored arcs are deleted. After a careful analysis of this procedure, however, we discovered that there are rare situations in which the DFS may fail to find an existing negative cy-

<sup>1</sup>In [2], the abbreviation ‘‘BFCT’’ refers to an implementation of the algorithm that does no updates, and a more complicated implementation of updates is considered. Our new implementation of updates has essentially no overhead, making the implementation with no updates obsolete.

cle in  $G_d$ , which could cause the algorithm to terminate later or not at all (although the latter never happened in the experiments reported in [2, 26]). Note that the problem is not in the original GOR algorithm, but in the way this particular implementation avoids contractions.

We propose and use a new implementation of GOR that corrects this problem while still avoiding contractions. It runs the SCC algorithm of Tarjan [22], which performs a single DFS while maintaining a second stack (in addition to the one used by the DFS) to store SCC-related information. When an SCC is fully processed, its vertices are at the top of the second stack. While the algorithm pops vertices of the SCC from the stack, our implementation of GOR examines their adjacency lists to check if there is a negative arc inside the SCC. If there is, the algorithm terminates with a negative cycle; otherwise, it produces a topological ordering of  $G_d$  with back arcs deleted. Vertices will be scanned in that order. The resulting implementation still runs in  $O(nm)$  time and is guaranteed to find a negative cycle in  $G_d$  if there is one. In our experiments, we observed that this new implementation has an additional overhead of about 20% on graphs without negative cycles.

As a heuristic to speed up the detection of “obvious” cycles, we also check for negative back arcs during the DFS, and stop immediately if one is found.

**5.3 RD.** We now discuss a new  $O(n)$ -pass algorithm, which we call *robust Dijkstra* (RD). Unlike previous methods, RD takes potentials into account when deciding which vertex to scan next. It does so by associating to each labeled vertex  $v$  a *key*, defined as the (strictly positive) difference between its *previous potential* (the value of  $d(v)$  during the last scan of  $v$ ) and its current potential. If  $v$  has not yet been scanned, we use its initial potential as its *previous potential*. Note that the *previous potential* does not change when a vertex is labeled.

The algorithm partitions the labeled vertices into two sets,  $Q$  and  $S$ :  $Q$  contains those that have yet to be scanned in the current pass, and  $S$  contains the rest. Set  $Q$  is maintained as a priority queue ordered by key values, and  $S$  as an ordinary queue. Initially, all vertices are labeled and have zero potential. At each step, we extract a vertex with maximum key from  $Q$  and scan it. When a vertex becomes labeled, it is added to  $S$  if it has been scanned during the current pass or to  $Q$  otherwise.<sup>2</sup> If the potential of a labeled vertex  $v$  in  $Q$  decreases, its key increases and an increase-key operation is performed on  $v$ . When  $Q$  becomes

empty, a new pass starts by moving vertices from  $S$  to  $Q$  and heapifying  $Q$ . The algorithm uses subtree disassembly with updates, which causes vertices in the affected subtrees to be removed from  $S$  or  $Q$ .

It may seem more natural to give priority to vertices with the lowest potentials (instead of highest improvement). In practice, however, it does not work well because a potential transformation may affect the vertex selection order in every pass. For example, if for some vertex  $v$  we decrease the length of incoming arcs by a large value and increase the length of outgoing arcs by the same amount,  $v$  will be scanned immediately after it is added to  $Q$ .

As its name suggests, RD is a generalization of Dijkstra’s algorithm: on the shortest path problem, these algorithms will scan the same sequence of vertices if arc lengths are nonnegative. (RD initialization is slightly different for the shortest path problem: the source potential is still set to zero, but all others must be set to  $M$ , a finite number larger than the length of any possible shortest path.) Note that, if there are negative input arcs, RD cannot use a monotone priority queue, such as multilevel buckets [7]; a general (and potentially less efficient) data structure must be used instead. Using Fibonacci heaps [12], the algorithm runs in  $O(n(m + n \log n))$  time. Our implementation, which we call RDH, uses a 4-heap [24] (which is more efficient in practice) and runs in  $O(nm \log n)$  time.

In an effort to reduce the data structure overhead, we also implemented RDB, which maintains an approximate bucket-based priority queue. Each bucket consists of a doubly-linked list of vertices that behaves as a queue with support for arbitrary deletions.<sup>3</sup> Bucket 0 contains vertices with key 0, and bucket  $i$  contains vertices with keys in  $[2^{i-1}, 2^i)$ . We also maintain an upper bound  $\mu$  on the index of the biggest-key nonempty bucket. This data structure supports the insert, delete, and increase-key operations in  $O(\log \log M)$  time and extract-max in  $O(\log M)$  time. This leads to an  $O(n(m \log \log M + n \log M))$  time bound for RDB.

**5.4 MBFCT.** Wong and Tan’s MBFCT algorithm [26] is a “local” variant of BFCT. All vertices start the algorithm unreached and with zero potential. The following procedure is executed while there are unreached vertices: Pick an unreached vertex  $v$ , mark it labeled, set  $p(v) = s$ , and run BFCT; terminate if a negative cycle is found, otherwise proceed to the next iteration. The potential function modified by BFCT is maintained throughout MBFCT, which means that only vertices whose potentials improve are visited in each new

<sup>2</sup>A variant that adds all newly labeled vertices to  $S$  proved to be less robust in our tests.

<sup>3</sup>We also tested buckets as stacks, with largely similar results.

iteration.

Since each call to BFCT can only reduce the number of unreached vertices, the algorithm terminates after at most  $n$  calls. It runs in  $O(n^2m)$  worst-case time, and our experiments include a family of instances that match this bound. Although this is not an  $O(n)$ -pass algorithm, in practice it is often faster than BFCT [26], especially on graphs with several small negative cycles. Unlike the original implementation of MBFCT, ours does updates during subtree disassembly, which sometimes makes it more efficient.

Note that there is some similarity between MBFCT and Pallottino’s algorithm [20]. The latter also works “locally” using the Bellman-Ford-Moore algorithm.

Next we sketch Pallottino’s algorithm. It partitions vertices into low- and high-priority sets. Initially every vertex has low priority, and priorities can only change from low to high. The algorithm maintains two queues,  $H$  and  $L$ , which contain labeled vertices of high and low priority, respectively. Initially,  $L$  contains all labeled vertices. The algorithm works in phases. At the beginning of each phase  $H$  is empty. The algorithm removes a vertex from the head of  $L$ , changes its priority to high, and adds it to  $H$ . Then the vertices in  $H$  are scanned in FIFO order. A scan can add vertices to both  $H$  and  $L$ , depending on the priority of the labeled vertex. The phase terminates when  $H$  becomes empty. (One can show that at this point the current potentials are feasible for the subgraph induced by the set of high-priority vertices.)

Two main differences between MBFCT and Pallottino’s algorithm are that MBFCT does not restrict scans to high-priority vertices and uses subtree disassembly (though Pallottino’s algorithm can also use subtree disassembly, as shown in [2]). Both algorithms run in  $O(n^2m)$  time, however, and our bad-case example for MBFCT, described in Section 6, is also bad for Pallottino’s algorithm.

**5.5 Other algorithms.** We experimented with several other algorithms. This section discusses some that, although natural, ended up being relatively less competitive in practice.

A natural approach is to combine the ideas of topological sort and subtree disassembly. One can either do GOR-like topological sorting at each pass of BFCT, or perform subtree disassembly (which may make mark some labeled vertices as unreached) while running GOR. We tried several variants of these ideas, but failed to get a robust performance improvement on our best implementations.

We also considered an algorithm that has potentially useful theoretical performance guarantees in the

incremental context. The *arc-fixing algorithm* (AF) is motivated by the chain-elimination procedure of [14]. To gain intuition, suppose the input graph as only one negative arc  $(v, w)$  with  $\ell(v, w) = -x$ . We can change  $\ell(v, w)$  to zero and apply Dijkstra’s algorithm to the resulting graph with  $w$  as the source. If the distance to  $v$  is less than  $x$ , the input graph has a negative cycle formed by  $(v, w)$  and the computed shortest path from  $w$  to  $v$ . Otherwise one can use the computed distances to get feasible potentials, “fixing” the arc  $(v, w)$ .

Next we describe the arc-fixing algorithm. All vertices start with zero potential. Each iteration begins by checking if the admissible graph  $G_d$  has a negative cycle. If so, the algorithm terminates. Otherwise, it tries to update the potentials by running two shortest path computations.

Let  $G'$  be the graph induced by the arcs out of the root vertex  $s$  (with length zero) together with all arcs  $(v, w)$  with  $\ell_d(v, w) < 0$ . This graph is acyclic. In linear time, we compute the distance  $\pi(v)$  in  $G'$  from  $s$  to each vertex  $v$ . Note that  $\pi(v) \leq 0$ .

The algorithm then builds a new graph  $G''$ , which has the same topology as  $G$ , but different arc lengths: arcs out of  $s$  have length  $\pi(v)$ , and the remaining ones have length  $\max\{0, \ell_d(a)\}$ . Since all negative arcs of  $G''$  are adjacent of  $s$ , Dijkstra’s algorithm correctly finds the distances  $p(v)$  in  $G''$  from  $s$  to each vertex  $v$  while scanning each vertex once. Note that  $p(v) \leq 0$ .

The algorithm completes an iteration by updating  $d(v)$  to  $d(v) + p(v)$ .

One can show that the arc-fixing algorithm has the following properties: (i) once an arc becomes nonnegative, it remains nonnegative; (ii) in each iteration, either there is a vertex that has outgoing negative arcs in the beginning but not at the end, or the next iteration discovers a negative cycle. Therefore, if the number of vertices with outgoing negative arcs in  $G$  is  $k$ , the algorithm terminates in  $k$  iterations. Although this is a promising bound for incremental scenarios, this algorithm was not competitive with the best algorithms in our study.

## 6 Worst-Case Instances

This section introduces graph families that elicit the worst-case behavior of the algorithms we implemented. We concentrate on the feasibility problem, but we note that simple modifications of these families give worst-case instances for the single-source shortest path problem as well. Within a family, the number of vertices in each network is a linear function of a parameter  $k$ , which indicates how many times a given gadget is repeated. The number of arcs is also linear in  $k$  for most families, but there are two cases in which it is

quadratic. To the best of our knowledge, these are the first families to match the theoretical worst-case bounds of any of the algorithms we study.

**6.1 BFCT.** The network  $\text{BAD-BFCT}(k)$  contains  $n = 4k - 1$  vertices and  $m = 5k - 3$  arcs. Vertices 1 to  $3k - 2$  together with the arcs  $(i + 1, i)$ ,  $1 \leq i \leq 3k - 3$ , form a path  $P$ . Every third vertex on  $P$  is connected to vertex  $3k - 1$ , i.e., we have the arcs  $(3(i - 1) + 1, 3k - 1)$  for  $1 \leq i \leq k$ . Finally,  $3k - 1$  is connected to vertices  $3k$  to  $4k - 1$ . All arcs in this graph have length  $-1$ . Figure 1 gives an example for  $k = 4$ .

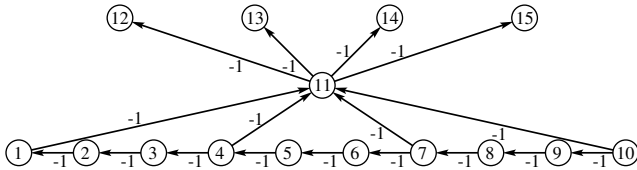


Figure 1: Worst-case input for BFCT ( $k = 4$ ).

Assume that the vertices are initially ordered in the BFCT queue by their identifier, from smallest to largest. During the first pass, the vertices on  $P$  are scanned in increasing order. After the first pass, the vertices on  $P$  (except the last vertex) are scanned once more, in decreasing order. Because of subtree disassembly, only one vertex on  $P$  is in the queue at a time. Consider what happens when vertex  $3i$  ( $1 \leq i < k$ ) is scanned for the second time. At this point only  $3i$  and  $3k - 1$  are in the queue. After scanning  $3i$ , vertex  $3i - 1$  is inserted into the queue following  $3k - 1$ . After scanning  $3k - 1$ , vertices  $3k, \dots, 4k - 1$  are inserted into the queue and scanned after  $3i - 1$ . Scanning  $3i - 2$  disassembles the subtree of  $3k - 1$  and produces a similar state as before scanning  $3i$ . The same pattern continues for  $j = i - 1, \dots, 1$ . This implies a total of  $\Theta(k^2) = \Theta(n^2)$  scans.

We note that by adding the arcs  $(3i - 1, 3k - 1)$ , the subtree of  $3k - 1$  is disassembled before scanning vertices  $3k$  to  $4k - 1$ , which results to  $O(1)$  scans per vertex. It is also interesting to note that without subtree disassembly the subgraph induced by  $P$  alone is a worst-case instance.

**6.2 MBFCT.** With a similar network we can construct a worst-case instance for MBFCT. To obtain  $\text{BAD-MBFCT}(k)$  from  $\text{BAD-BFCT}(k)$  we first reverse the orientation of the path  $P$ . Let  $G(k)$  be the resulting graph. Then we add new vertices  $4k, \dots, 6k - 1$ , which are connected to the extremes of  $P$  as follows: the even vertices are connected to vertex 1, and the odd vertices are con-

nected to  $3k - 2$ . The length of the arc leaving vertex  $4k + i$  is  $-4k(i + 2)$ ,  $0 \leq i \leq 2k - 1$ . Figure 2 gives an example for  $k = 4$ . Note that  $n = 6k - 1$  and  $m = 7k - 3$ .

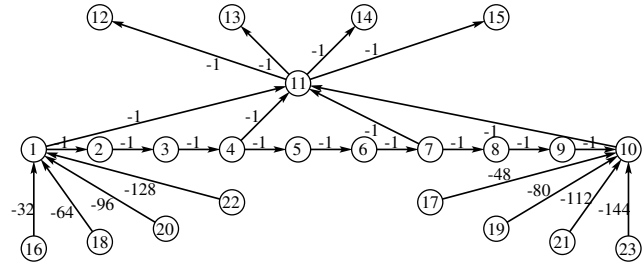


Figure 2: Worst-case input for MBFCT ( $k = 4$ ).

After vertex  $4k + 2i$  is scanned, MBFCT needs  $\Theta(k^2)$  scans to process  $G(k)$ . The role of each vertex  $4k + 2i + 1$  is to disassemble the subtree rooted at  $3k - 1$ , so that the next pass over  $G(k)$  will still require  $\Theta(k^2)$  scans. This gives a total of  $\Theta(k^3) = \Theta(n^3)$  scans.

We note that, if we connect an artificial source to all original vertices with arcs of length zero, then we obtain a worst-case ( $\Theta(n^3)$  scans) instance for Pallottino's shortest paths algorithm [20].

**6.3 GOR.** The worst-case network  $\text{BAD-GOR}(k)$  consists of a path  $P$  of  $k$  vertices  $1, \dots, k$ , a vertex  $k + 1$  with  $k$  incoming and  $k$  outgoing arcs, and  $k$  vertices  $k + 2, \dots, 2k + 1$ . Set  $\ell(1, 2) = -3k$ ,  $\ell(1, k + 1) = -1$ , and  $\ell(i, i + 1) = 1$  for  $2 \leq i \leq k - 1$ . Also,  $\ell(k + 1, k + 1 + i) = -1$  for  $1 \leq i \leq k$ , and  $\ell(i, k + 1) = 2(k - i)$  for  $2 \leq i \leq k$ . We have  $n = 2k + 1$  and  $m = 3k - 1$ . Figure 3 gives an example for  $k = 7$ .

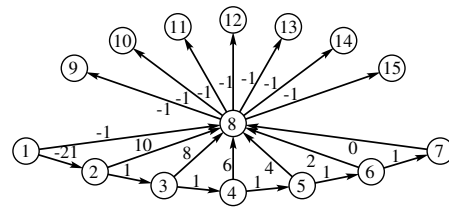


Figure 3: Worst-case input for GOR ( $k = 7$ ).

Note that GOR needs  $\Theta(k)$  passes to process  $P$ : Initially the only admissible arc on  $P$  is  $(1, 2)$ , and scanning vertex 2 makes  $(2, 3)$  admissible. These are the only arcs on  $P$  that are considered during the first pass of the algorithm. Each subsequent pass makes two more arcs on  $P$  admissible. The arcs leaving vertex  $k + 1$  remain admissible in each pass, because the length of the path  $(1, 2, \dots, i, k + 1)$  is smaller than the length of

$(1, 2, \dots, i, i + 1, k + 1)$  for  $i = 2, \dots, k - 1$ . This causes GOR to scan vertices  $k + 1, \dots, 2k + 1$  in each pass. It follows that the total number of scans is  $\Theta(k^2) = \Theta(n^2)$ .

**6.4 RD.** The elaborate rule that RD employs for selecting the next vertex to scan (combined with subtree disassembly with updates) makes it hard to construct worst-case instances. We can simplify the problem slightly by making additional assumptions on how the priority queue breaks ties. This is the case in our implementation of RDB, since each bucket is implemented as a queue. But in a heap-based implementation of RDH it is hard to predict which element among the ones with maximum key will be returned. Our implementations of RDB and RDH do exhibit quadratic running time for the worst-case families we created, even though the latter does not use any special tie-breaking rule. The reader should be aware, however, that other implementations may behave differently.

For an intuition on the construction of the worst-case networks, consider the graph  $G(k)$  shown in Figure 4. It consists of  $k$  arcs  $(x_i, y_i) = (2i - 1, 2i)$  for  $1 \leq i \leq k$ , together with the arcs  $(x_i, x_{i+1})$  and  $(y_i, x_{i+1})$  for  $1 \leq i < k$ . We require each path  $(x_i, y_i, x_{i+1})$  to be shorter than  $(x_i, x_{i+1})$ . To that end we can set  $\ell(x_i, y_i) = 0$ ,  $\ell(y_i, x_{i+1}) = -2$  and  $\ell(x_i, x_{i+1}) = -1$ . In the first pass of RD, vertex  $x_{i+1}$  has priority over  $y_i$ ; hence all the  $x_i$ 's will be scanned first, and then all the  $y_i$ 's. In the following passes, when  $x_i$  is scanned  $y_i$  and  $x_{i+1}$  get the same priority, equal to the distance improvement for  $x_i$ . Suppose that  $(x_i, x_{i+1})$  is processed before  $(x_i, y_i)$ . Then, assuming that the priority queue of RD favors vertices that are inserted more recently,  $x_{i+1}$  will be scanned first. In this case, RD needs  $\Theta(k)$  passes<sup>4</sup> to compute the shortest path from 1 to  $2k$ . The total number of scans is therefore  $\Theta(k^2) = \Theta(n^2)$ .

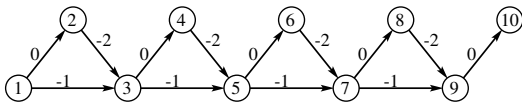


Figure 4: Worst-case input for RD ( $k = 5$ ) when the priority queue returns the most recently inserted vertex among all those with maximum key.

The graph  $G(k)$  is actually a worst-case input for the version of RDB where each bucket is implemented as a stack, since among all vertices in the same bucket the one most recently inserted will be preferred. If we

<sup>4</sup>The number of passes is exactly  $k + 1$  if subtree disassembly is not used, and  $k/2 + 1$  if it is.

represent each bucket as a queue (as in our implementation), however, RDB can process this graph with a linear number of scans. We can get a worst-case instance for this version of RDB as follows.

First, we swap the order in which the arcs leaving each odd vertex in  $G(k)$  are processed, so that when  $x_i$  is scanned,  $x_{i+1}$  is inserted into the queue before  $y_i$ . To simplify the discussion, consider a shortest path computation starting from  $x_1$  and assume that all labeled vertices are inserted in a single bucket. (Similar arguments to the ones below apply to the actual RDB algorithm for the feasibility problem.) After  $x_1$  is scanned,  $x_2$  and  $y_1$  are inserted, in this order, into the queue, and  $x_2$  is scanned before  $y_1$ . When  $y_1$  is scanned, the distance to  $x_2$  is improved but the current pass ends, as  $x_2$  was already scanned. Then, the second pass begins with  $x_2$  being scanned again. The same pattern continues until all odd vertices (except  $x_1$ ) are scanned twice and all even vertices are scanned once, giving a total of  $k$  passes and  $\Theta(k)$  scans in total.

To make the number of scans quadratic, we augment this graph with a set of new vertices that will be scanned in every pass. We connect each even vertex  $y_i$  to a new vertex  $2k + 1$  and connect  $2k + 1$  to  $k$  new vertices  $2k + 2, \dots, 3k + 1$ . All new arcs have length  $-1$ . The new graph has  $n = 3k + 1$  vertices and  $m = 5k - 2$  arcs. Figure 5 gives an example for  $k = 5$ . The new vertices will be scanned in each pass, thus resulting in  $\Theta(k^2)$  scans.

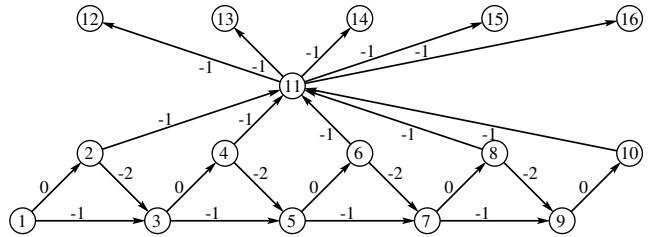


Figure 5: Worst-case input for RDB and RDH ( $k = 5$ ).

The experimental results in Section 7.2 (Table 6) suggest that our implementation of RDH also exhibits quadratic number of scans for the graph of Figure 5. We refer to this family as BAD-RD( $k$ ).

It is interesting to note that another graph inducing  $\Theta(k^2)$  scans for RDB is a complete directed acyclic graph (DAG) on  $k$  vertices. In this graph we have an arc  $(i, j)$  with length  $-1$ , for each  $i < j$ . Also, the experimental results suggest that RDH requires a superlinear number of scans for this family, which we call COMP-DAG( $k$ ). Note that, unlike all previous families, these graphs are dense.

**6.5 Arc-fixing algorithm.** This is another family of dense graphs. We have  $n = 3k + 2$  and  $m = k^2 + 4k + 1$ . To construct a graph, we start with a path  $P = (1, 2, \dots, 2k + 2)$  where the arcs lengths alternate between  $-1$  and  $1$ . Then we add  $k$  vertices,  $2k + 3$  to  $3k + 2$ . For  $0 \leq i \leq k - 1$ , vertex  $2k + 3 + i$  has an incoming arc of length  $k + 1 - i$  from  $2i + 1$ , and  $2(k - i)$  unit-length outgoing arcs to  $2i + 3, 2i + 4, \dots, 2k + 2$ . Figure 6 gives an example for  $k = 3$ .

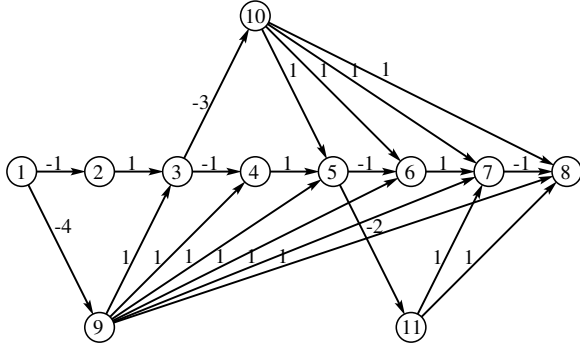


Figure 6: Worst-case input for the arc-fixing algorithm ( $k = 3$ ).

Each iteration of the arc-fixing algorithm causes the reduced cost of one negative arc on  $P$  to become zero. At the end of the  $i$ -th iteration all vertices on the path  $P_i = (2i + 3, 2i + 4, \dots, 2k + 2)$  have equal potentials, so the reduced costs of the arcs on  $P_i$  do not change. Therefore, the algorithm requires  $\Theta(k) = \Theta(n)$  iterations. Since each iteration runs Dijkstra’s algorithm, the total running time is  $\Omega(mn)$ .

## 7 Experiments

**7.1 Experimental setup.** The improved implementations of BFCT and GOR, the most robust algorithms in previous studies, are the baseline of our experimental evaluation. We also include MBFCT, for which [26] gives encouraging results. Finally, we test our new algorithms, RDH and RDB, which are superior on some problem families.

Our main measure of performance is the number of scans per vertex, which is machine-independent. It includes both shortest path scans and auxiliary scans (such as those during DFS). All results reported are averages taken over 10 (or more, when noted) instances with different pseudorandom generator seeds.

We also report some running times to measure the data structure overhead for different algorithms. Our timed experiments were run on a 3 GHz Intel Xeon with 32 GB of RAM running Red Hat Enterprise Linux

5. For readability and succinctness, however, we omit running times for most experiments.

All algorithms were implemented in the same style and made as efficient as possible. They were coded in C++ and compiled with gcc v. 4.1.2 with the `-O4` flag. Arc lengths and potentials were represented as 32-bit signed integers.

**7.2 Feasibility.** We start with the standard (non-incremental) feasibility problem. As seen in [2], the number of negative cycles and their cardinality (number of arcs) can have a significant effect on performance. For a tight control over the structure of the instances, we created a filter called NEGCYCLE. It takes as input an arbitrary base graph with no negative arcs and adds vertex-disjoint negative cycles to it. Every arc on these cycles has length zero, except for one with length  $-1$ . As in [2], from each base graph family we create five subfamilies: (01) no negative cycles; (02) one negative cycle with three arcs (small cycle); (03) many small negative cycles; (04) a constant number of medium negative cycles; and (05) one Hamiltonian negative cycle.

After adding the negative cycles (or not, in case 01), we apply a potential transformation to “hide” them. A potential transformation with parameter  $X$  selects, for each vertex  $v$ , an integer  $d(v)$  uniformly at random from  $[0, X)$ , adds  $d(v)$  to each of  $v$ ’s incoming arcs, and subtracts  $d(v)$  from the outgoing arcs; the effect is to replace lengths by reduced costs. Unless otherwise noted, we use  $X = 1000$ .

Our testbed includes the graph families tested in [2], augmented with road networks and worst-case examples. We discuss each family in turn.

**7.2.1 Random graphs.** The SPRAND generator [3] creates an instance with  $n$  vertices and  $m$  arcs by building a Hamiltonian cycle on the vertices and adding  $m - n$  arcs at random. All arc lengths are selected uniformly at random from  $[L, U]$ .

In our first experiment, we set  $L = 0$  and  $U = 1000$  and fix  $m = 5n$  (results were similar with other graph densities). We vary  $n$ , and apply the NEGCYCLE filter in each case. Table 1 shows the average number of scans per vertex for this family (which we call RAND5).

Note that the number of scans per vertex barely increases (if at all) with graph size, which suggests that on these graphs the algorithms perform much better than their worst-case bounds. For the 03 subfamily (which has many cycles of size 3), most algorithms actually do better (relative to  $n$ ) as the graph size increases. The main exception is GOR, which performs a first pass that visits essentially every arc in the graph,

Table 1: Feasibility: Scans per vertex on RAND5.

FAM	$n$	BFCT	MBFCT	GOR	RDB	RDH
01	262144	1.0105	1.0107	1.1016	1.0107	1.0180
	524288	1.0105	1.0107	1.1013	1.0106	1.0180
	1048576	1.0105	1.0107	1.1019	1.0106	1.0181
	2097152	1.0105	1.0106	1.1019	1.0106	1.0182
02	262144	0.9813	0.5803	1.0939	0.9311	0.7954
	524288	0.9713	0.4916	1.0934	0.7703	0.7805
	1048576	1.0087	0.6095	1.0939	0.9508	0.7082
	2097152	1.0078	0.5313	1.0938	0.7200	0.7851
03	262144	0.0798	0.0001	1.2186	0.0001	0.0001
	524288	0.0667	$<10^{-4}$	1.1947	$<10^{-4}$	$<10^{-4}$
	1048576	0.0397	$<10^{-4}$	1.2191	$<10^{-4}$	$<10^{-4}$
	2097152	0.0319	$<10^{-4}$	0.9882	$<10^{-4}$	$<10^{-4}$
04	262144	2.1171	0.2050	3.3318	1.9126	1.8541
	524288	2.1399	0.3257	3.3741	1.9669	1.8826
	1048576	2.1677	0.4349	3.4460	1.9929	1.9222
	2097152	2.1388	0.3724	3.5164	1.9872	1.9083
05	262144	5.2216	3.0555	10.6780	4.6662	4.6957
	524288	5.1291	2.5635	10.7117	4.6237	4.5978
	1048576	5.1830	3.0098	10.9752	4.6047	4.5592
	2097152	4.9904	2.5356	10.9011	4.5558	4.6200

but only detects a cycle when processing the SCCs in the second pass. For this family, MBFCT, RDH and RDB, which tend to concentrate on a small region of the graph, do better than BFCT, which takes a more global approach. For the 04 subfamily, MBFCT is the winner.

The results of our second experiment, also on random graphs, are shown in Figure 7. Here we fix  $n = 65536$ ,  $m = 5n$ , the maximum arc length  $U$  at 1000, and vary the minimum allowed arc length  $L$  from 0 to  $-1200$ . We apply a potential transformation with parameter 2000 to the resulting graph (this is not done in [2], but it leads to more interesting results). The figure shows the average number of scans per vertex (over 50 different random seeds) as a function of  $L$ . Every instance with  $L \leq -100$  had a negative cycle.

All algorithms are faster for lower values of  $L$ , when negative cycles are more numerous. RDH needs the fewest scans to find such cycles, in some cases winning by several orders of magnitude—the priority queue helps finding “obvious” cycles by focusing on a small number of candidate paths. The bucket-based version of the same algorithm, RDB, cannot find a cycle as fast: since buckets are organized as queues, the algorithm tends to expand many candidate paths at once. In fact, preliminary experiments have shown that organizing buckets as stacks (which tends to favor the most recently improved path) would make the algorithm about as fast as RDH for this problem family, but the stack-based implementation is less robust in general.

Both BFCT and GOR are not much slower than RDH

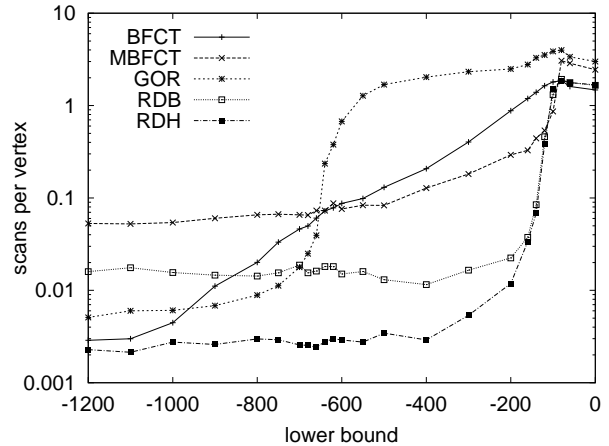


Figure 7: Feasibility on random graphs with 65536 vertices and arc lengths in  $[L, 1000]$ , with  $L$  variable.

when  $L$  is very negative, but degrade as it increases. GOR does so suddenly, at around  $L = -650$ , when the cycle-detection heuristic (which looks for negative back arcs during the DFS) ceases to be effective. BFCT degrades more slowly, but soon becomes worse than MBFCT.

**7.2.2 Grids.** We also tested the algorithms on *grid networks*, which are grids embedded on a torus, created by the TOR generator [2]. Specifically, vertices correspond to points on the plane with integer coordinates  $[x, y]$ ,  $0 \leq x < X$ ,  $0 \leq y < Y$ . These points are connected “upward” by *layer* arcs of the form  $([x, y], [x, y + 1 \bmod Y])$ , and “forward” by *inter-layer* arcs of the form  $([x, y], [x + 1 \bmod X, y])$ . In addition, there is a source connected to all vertices with  $x = 0$ . Lengths are chosen uniformly at random from  $[1, 100]$  for layer arcs, and from  $[1000, 10\,000]$  for inter-layer arcs (including those from the source). Once the grid is formed, we apply the NEG-CYCLE filter to create distinct subfamilies.

Table 2 shows the average number of scans per vertex on *square grids with negative cycles* (SQNC), for which  $X = Y$ . All five subfamilies are shown.

This family is slightly harder than RAND5 for all algorithms, but their relative performance remains largely unchanged. An important exception is BFCT becoming faster than MBFCT on subfamily 05, which has a Hamiltonian negative cycle. Also, RDB outperforms RDH on the first four subfamilies.

Experiments with *long grids with negative cycles* (LNC), which have  $X = n/16$  layers with  $Y = 16$  vertices each, yielded very similar results, as Table 3











